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PATENT

ON 680 22 6 Atry. Dkr. AMAT/3083.P1/FET/FET/DV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Reginald Hunter

Serial No.:

UNKNOWN

Filed:

HEREWITH

For:

Method and Apparatus for Enhanced

Embedded Substrate Inspection

Through Process Data Collection and §

Substrate Imaging Techniques

Assistant Commissioner for Patents Washington, D.C. 20231

5- Willie 4-19-01

Group Art Unit:

UNKNOWN

Examiner:

UNKNOWN

CERTIFICATION UNDER 37 CFR 1.10

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Date

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Dear Sir:

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The applicants, and the attorney who signs below on the basis of the information supplied by the inventor and the information in his file, submit herewith patents, publications, or other information of which they are aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56.

While the information submitted in this Information Disclosure Statement may be material pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any patent, publication, or other information referred to therein is prior art for this invention unless specifically designated as such.

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The patents and/or publications submitted herewith are set forth on the attached Form PTO-1449.

Respectfully submitted,

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